

Notification Number:	20210519002A	Notification Date:	May 24, 2021
Title:	Datasheet for DRV880x, DRV8801-Q1, DRV8801A-Q1		
Customer Contact:	Notification Manager	Dept:	Quality Services
Change Type:	Electrical Specification		

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is being updated as summarized below. The following change history provides further details.



DRV8800, DRV8801
SLVS855K – JULY 2008 – REVISED MARCH 2021

Changes from Revision J (July 2014) to Revision K (March 2021)	Page
• Added missing GND pin reference to <i>Pin Functions</i> Table	3
• Added SENSE pin to <i>Pin Functions</i> Table	3
• Improved description for pins nFAULT, nSLEEP, VBB and VCP in <i>Pin Functions</i> Table	3
• Added entries for VCP and CP2 pins in <i>Absolute Maximum Ratings</i> table.....	5
• Added VDD pin voltage limits and device's PWM frequency limits to <i>Recommended Operation Conditions</i> table.....	5
• Changed SLEEP to nSLEEP in PWM Control Timing Figure.....	8
• Added equation for VPROPI to help when connecting pin's output to ADC in <i>Feature Description</i>	12
• Provide additional information on SENSE pin behavior.....	13
• Added device specific details to <i>Control Logic</i> Table	13
• Added die temperature estimation equation utilizing junction to ambient thermal impedance in <i>Application and Implementation</i> section.....	18
• Added information on using motor driver's pulse width modulating modes in <i>Application and Implementation</i> section.....	18



DRV8801-Q1
SLVSAS7D – FEBRUARY 2011 – REVISED MARCH 2021

Changes from Revision C (June 2016) to Revision D (March 2021)	Page
• Improved description for pins CP1,CP2,nFAULT, nSLEEP, VBB and VCP in <i>Pin Functions</i> table.....	4
• Added entries for VCP and CP2 pins in <i>Absolute Maximum Ratings</i> table.....	5
• Change SLEEP to nSLEEP in PWM Control Timing Figure.....	8
• Provide additional information on SENSE pin behavior.....	13
• Added equation for VPROPI to help when connecting pin's output to ADC in <i>Feature Description</i>	14
• Added die temperature estimation equation utilizing junction to ambient thermal impedance in <i>Application and Implementation</i> section.....	17
• Added information on using motor driver's pulse width modulating modes in <i>Application and Implementation</i> section.....	18
• Added information on connecting multiple DRV8801-Q1 together to support higher current in <i>Application and Implementation</i> section.....	19

Changes from Revision C (July 2016) to Revision D (June 2020)	Page
• Improved description for pins CP1, CP2, nFAULT, nSLEEP, VBB and VCP in <i>Pin Functions</i> table.....	3
• Added entries for VCP and CP2 pins in <i>Absolute Maximum Ratings</i> table.....	4
• Removed incorrect duplicate input logic current entry for ENABLE pin in <i>Electrical Characteristics</i> table.....	5
• Added additional information on SENSE pin behavior.....	10
• Added equation for VPROPI to help when connecting pin's output to ADC in <i>Feature Description</i>	11
• Added die temperature estimation equation utilizing junction to ambient thermal impedance in <i>Application and Implementation</i> section.....	14
• Added information on using motor driver's pulse width modulating modes in <i>Application and Implementation</i> section.....	14
• Added information on connecting multiple DRV8801-Q1 together to support higher current in <i>Application and Implementation</i> section.....	15

The datasheet number will be changing.

Device Family	Change From:	Change To:
DRV880x	SLVS855J	SLVS855K
DRV8801-Q1	SLVSAS7C	SLVSAS7D
DRV8801A-Q1	SLVSC79C	SLVSC79D

These changes may be reviewed at the datasheet links provided.

- <http://www.ti.com/product/DRV8800>
- <http://www.ti.com/product/DRV8801-Q1>
- <http://www.ti.com/product/DRV8801A-Q1>

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this notification:

None.

Product Affected:

DRV8800PWP	DRV8800PWPR	DRV8800RTYR	DRV8800RTYT
DRV8801PWP	DRV8801PWPR	DRV8801RTYR	DRV8801RTYT
DRV8801QRTYRQ1	DRV8801AQRMJRQ1		

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